## **IN THE SPECIFICATION**

Cancel the section "RELATED APPLICATIONS" and substitute therefor the following new section:

## **RELATED APPLICATIONS**

[01] This application makes reference to, claims priority to, and claims the benefit of United States Provisional Patent Application No. 60/516,777 (attorney docket number 15149US01) filed on November 3, 2003, entitled "Setting Numerically Controlled Delay Lock Loops Using Step Sizes Based on Delay," the complete subject matter of which is hereby incorporated herein by reference, in its entirety.

[02] This application makes reference to United States Provisional Patent Application 60/485,597 (attorney docket number 15072US01) filed on July 8, 2003, entitled "Scheme for Optimal Settings of DDR Interface," the complete subject matter of which is hereby incorporated herein by reference, in its entirety.